

# (OPTOELECTRONIC DIV.)

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# HG322481BNHDWB-V2 ROHS DATA SHEET

Acceptance

ISSUE	VERSION	<b>APPROVER</b>	<b>CHECKER</b>	<b>ENGINEER</b>
筝典 02/25 Jy-Hao	A	筝典 02/25 Edward		<b>拿典</b> 02/25 Alan

Messrs.				
<b>Product Specification</b>	Model	HG322481BNHDWB-V2	Rev. NO.	Issued Date.
1 roduct Specification	Miduel.	11G322461DM11DWD-V2	Α	Feb.25.13

# **Records of Revision**

Revision	<b>Revision Date</b>	Contents	Approved
A	2013/02/25	Initial Release and Issue Full Specification	Alan



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1 Todact Specification	widuci.	110322401D1111D W D- V 2	A	Feb.25,13

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### 1. SCOPE

This specification covers the engineering requirements for the HG322481BNHDWB-V2 liquid crystal module.

### 2. PRODUCT SPECIFICATIONS

### 2.1 General

- $320 \times 240$  dot matrix LCD
- STN, blue mode transmissive type
- Negative, Wide temperature type
- Back-light: LED edge light, White, High brightness
- Multiplexing driving: 1/240 duty, 1/14 bias
- Drive IC: Neotec (NT7086PQ)
- R23: 0 ohm

# ROHS

### 2.2 Mechanical Characteristics

Item	Characteristic
Dot configuration	320 × 240
Dot dimensions(mm)	$0.34 \times 0.34$
Dot spacing (mm)	0.02
Module dimensions (Horizontal × Vertical × Thickness, mm)	$167.0 \times 109.0 \times 11.0 \text{ max}.$
Viewing area (Horizontal × Vertical, mm)	120.0 × 90.0
Active area (Horizontal × Vertical, mm)	115.17 × 86.37



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### 2.3 Absolute Maximum Ratings (Without LED back-light)

Characteristic	Symbol	Unit	Value
Power Supply Voltage (logic)	$V_{DD}$	V	-0.3 to +0.7
Power Supply Voltage (LCD)	$V_{DD}$ - $V_{EE}$	V	0 to 35
Input Voltage	V <sub>I</sub>	V	-0.3 to V <sub>DD</sub> +0.3

Note 1:  $V_{DD} \ge V_1 > V_3 > V_4 > V_{EE}$ 

### 2.4 Electrical Characteristics (Without LED back-light)

Characteristic	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Voltage(logic)	$V_{DD}$		2.7		5.5	V
Supply Voltage(LCD)	$V_{DD}$ - $V_{EE}$	Note 2,3	12		32	V
Innut Valtaga	$V_{ m IH}$		$0.8~\mathrm{V_{DD}}$		$V_{ m DD}$	V
Input Voltage	$V_{\rm IL}$		0		$0.2~\mathrm{V_{DD}}$	v
Output	$V_{OH}$		V <sub>DD</sub> -0.4		VDD	V
Voltage	$V_{ m HL}$		0		0.4	l v
CP (shift clock)	$f_{CP}$	СР			6.0	MHz
CP pulse width	$t_{WC}$	СР	50			ns
Load pulse width	$t_{ m WL}$	LOAD	50			ns
Setup time	$t_{SETUP}$	D11 to D14 <b>→</b> CP	30			ns
Hold time	$t_{HOLD}$	D11 to D14→CP	30			ns
CP→LOAD	$t_{\mathrm{CL}}$	CP→LOAD	80			ns
LOAD <b>→</b> CP	$t_{LC}$	LOAD <b>→</b> CP	110			ns
CP and LOAD rise time	t <sub>R</sub>	CP,LOAD			Note 4	ns
CP and LOAD fall time	$t_{\mathrm{F}}$	CP,LOAD			Note 4	ns

Note 2:  $V_{DD} \ge V_1 > V_3 > V_4 > V_{EE}$ 

Note 3: When the power is turned on ,either the logic system power must e turned on before the LCD drive system power or else they must both be turned on at the same time. When the power is turned off, either the LCD drive system power must be turned off before the logic system power, or else both must be turned off at the same time.

Note 4: The CP and LOAD rise time  $(t_R)$  and the CP and LOAD fall time  $(t_F)$  must satisfy equations  $\bigcirc$  and  $\bigcirc$  below at the same time.

$$@t_R, t_F < 50 ns$$



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# 2.5 Optical Characteristics Absolute maximum ratings

Item	Symbol	Rating	Unit
Operating temperature range	Тор	-20~70	°C
Storage temperature range	Tst	-30~80	°C

# 2.6 Optical Characteristics

1/240 duty, 1/14 bias, Vop=23.5V

				•		-
Item	Symbol	Temp.	Min.	Тур.	Max.	Unit
		-20 °C	22.8	23.5	24.2	
Driving voltage	Vop	25 °C	23.0	23.5	24.0	V
voitage		70 °C	20.8	21.5	22.1	
Contrast ratio	CR	φ=0° θ=0°	4.5	6.0	8.0	
Frame freq.	fF			70		Hz
Viewing	$\theta_1$			43		deg.
angle* $\theta_2$	$\theta_2$	25 °C		23		ucg.
Response	t <sub>on</sub>	25 °C		157		ms
time	$t_{\rm off}$	25 C		255		1113

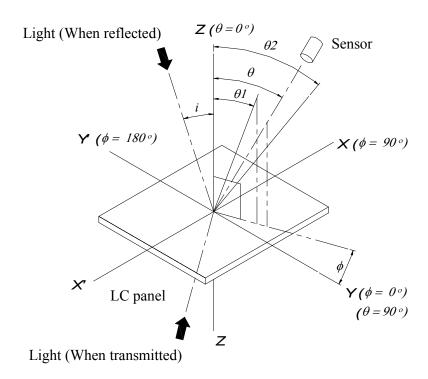


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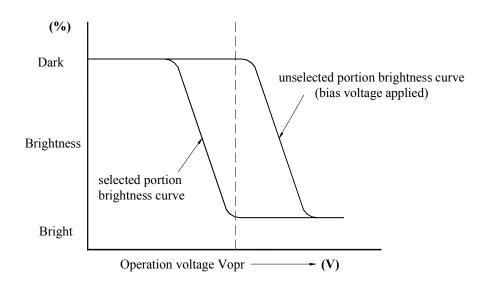
## 2.6.1 Definition of optical characteristics

\*Definition of angles  $\phi$  and  $\theta$ 



\*Definition of contrast C

$$C = \frac{B1}{B2} = \frac{\text{Brightness of selected portion}}{\text{Brightness of unselected portion}}$$

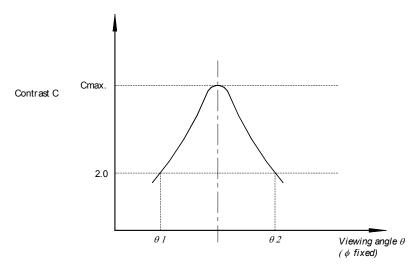




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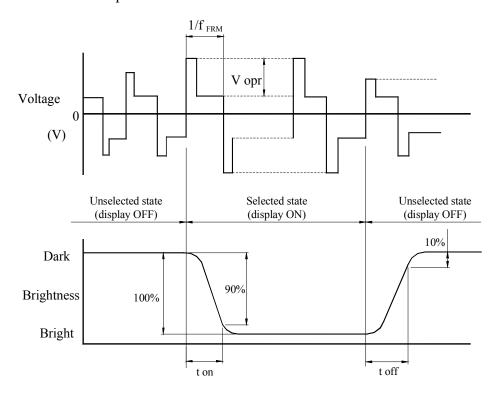
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\*Definition of viewing angles  $\theta 1$  and  $\theta 2$ 



Note : Optimum vision with the naked eye and viewing angle  $\theta$  at Cmax above are not always the same.

# \*Definition of response time





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Vop : Operating voltage (V) ton : Response time (rise) (ms) fFRM : Frame frequency (Hz) toff : Response time (fall) (ms)

### 2.7 LED Back-light Characteristics

# 2.7.1 Absolute maximum ratings

Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward voltage	$ m V_{ m f}$	If=200mA,		3.3	4.0	V
Forward voitage	<b>V</b> f	White		5.5		v
*Luminous Intensity	$I_{ m V}$	If=200mA,		600		cd/m*2
Lummous Intensity	ΙV	White		000		
Reverse Current	$I_R$	VR=5V,			0.5	mA
Reverse Current	1R	White				
*Chromaticity	X	If=200mA,	0.26	0.31	0.36	
coordinate	Y	White	0.26	0.32	0.36	
Luminous Tolerance	IV-m	If=200mA White	70		%	(Min/Max) *100%

Note: \* Measured at the bare LED back-light unit.

### 2.7.2 LED Maximum Operating Range

Item	Symbol	White	Unit
Power Dissipation	$P_{AD}$	800	mW
Forward Current	$I_{\mathrm{F}}$	200	mA
Reverse Voltage	$V_R$	5	V



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# 3. OPERATING INSTRUCTIONS

# 3.1 Input signal Function

CN2

NO.	Symbol	Function
1-4	D0-D3	Display Data
5	/DISPOFF	H: on L: off
6	FLM/NC	First line market signal / not connecting
7	M	Frame reverse signal
8	LP	Data latch frequency
9	СР	Data shift frequency
10	VDD	Power supply for Logic circuit (+)
11	VSS	Ground (0V)
12	VEE	Power supply for LCD
13	VO	Contrast adjust for LCD
14	FGND	Front panel ground(Bezel ground)



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### CN3

NO.	Symbol	Function
1	FG	Front panel ground(Bezel ground)
2	/DISPOFF	H: on L: off
3	FLM/NC	First line market signal / not connecting
4	CL1	Data latch frequency
5	CL2	Data shift frequency
6	VSS	Ground (0V)
7-10	D0-D3	Display Data
11	VSS	Ground (0V)
12	VDD	Power supply for Logic circuit (+)
13	VO	Contrast adjust for LCD
14	VEE	Power supply for LCD
15	VSS	Ground (0V)
16	VSS	Ground (0V)

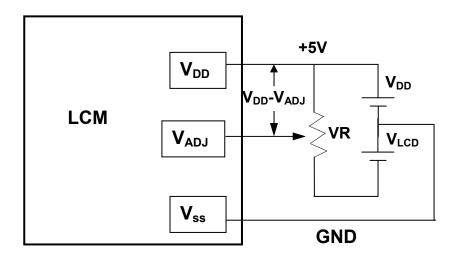


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### 3.2 Voltage Generator Circuit

# **Power Supply Circuit Diagram**



 $V_{DD}$  –  $V_{ADJ}$ : LCD Operating Voltage VR : 10K~20K

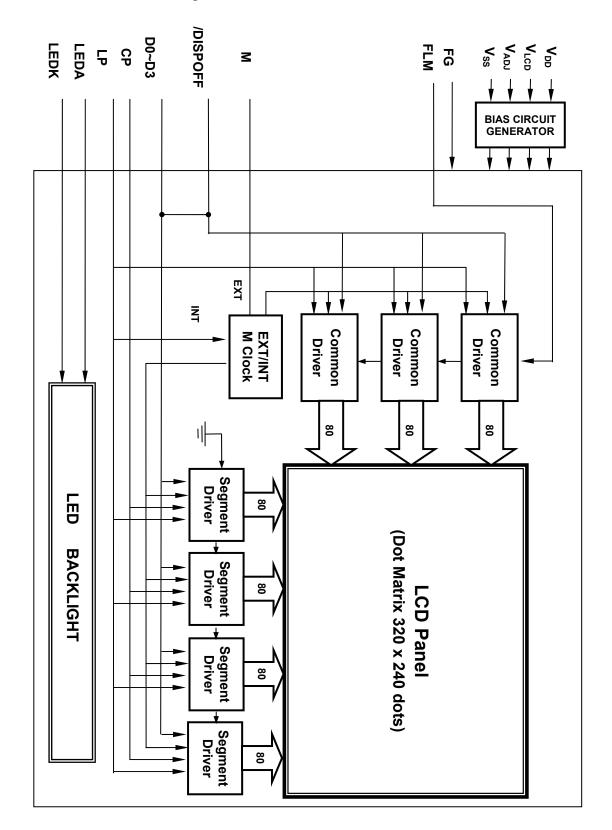




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### 3.3 Circuit Block Diagram

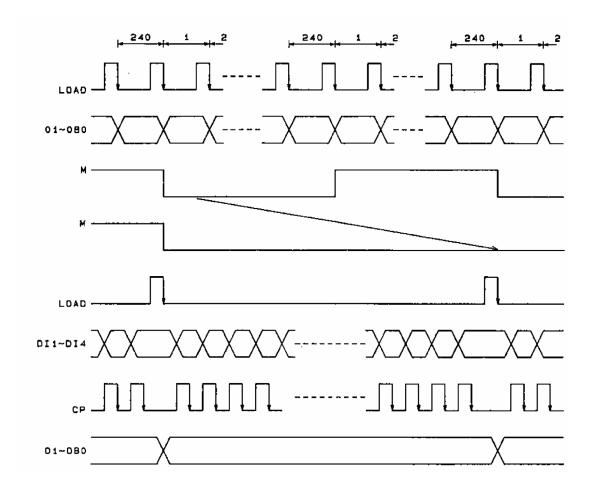




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# 3.4 Timing Characteristics





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### 4. Unit driving method

4.1 Ration Circuit configuration

Fig. 8-1 shows the block diagram of the unit circuitry.

4.2 Input data and control signal

The LCD driver is 48 bits LSI, consisting of shift registers, latch circuits and LCD driver circuits.

Display data which are externally divided into data for each row (48 dots) will be sequentially transferred in the from of serial data.

### 5. Quality level

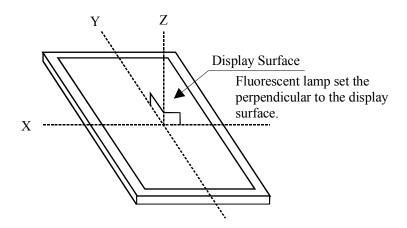
- 5.1 Inspection conditions
  - 5.1.1 The environmental conditions for inspection shall be as follows.

Room temperature :  $20 \pm 3$  °C Humidity :  $65 \pm 20$ %RH

5.1.2 The external visual inspection

The inspection shall be performed by suing a single 20W fluorescent lamp for illumination and the distance from LCD to eyes of the inspector should be 30cmj or more.

### 5.1.3 (1) Light method

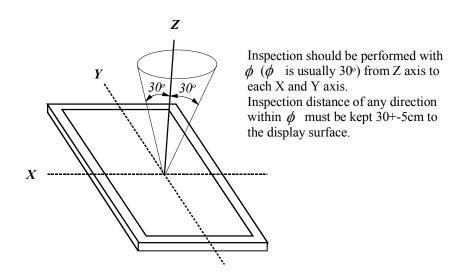




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### (2) Inspection distance and angle



### 5.2 Sampling procedures for each item's acceptance level table.

Defect type	Sampling procedures	AQL
	MIL-STD-105D Inspection level I	
Major defect	Normal inspection	1.0
wagor derect	Single sample inspection	1.0
Minor defect	Normal inspection	2.5
Trimor defect	Single sample inspection	2.3

### 5.3 Classification of defects

### 5.3.1 Major defect

A major defect refers to a defect which is not considered to substantially degrade usability for project applications.

### 5.3.2 Minor defect

A minor defect refers to a defect which is not considered to substantially degrade usability applications, or a defect which deviates from existing standards almost unrelated to the effective use of the product or it's operations.



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# 5.4 Incoming Inspection standards

		iispection standards	Cuitanian Can 1-Can		Dafagtt
	Item		Criterion for defects		Defect type
(1)	Display on	(1) Non display			
	inspection	(2) Vertical line is deficient			Major
		(3) Horizontal line is	deficient		
		(4) Cross line is defi	cient		
		Size (mm)	Acceptab	e number	
(2)	Black/	≤ 0.15	Ignore	(note)	
(-)	White spot	$0.15 < \leq 0.20$	3	3	3.41
		$0.20 < \leq 0.30$	]	•	Minor
		0.30 <	()	)	
		(Note) If more than 4 Length (mm)	Width (mm)	Acceptable number	
		$L \leq 10$	$W \leq 0.03$	Ignore (note)	
(3)	Black/	$\frac{L \leq 10}{10 < L \leq 5.0}$	$0.03 < W \le 0.06$	3	
	White line	$5.0 < L \le 3.0$	$\frac{0.05 < W \le 0.00}{0.06 < W \le 0.08}$	2	
		Defects separate at int		_	
		s-parave at III		-	Minor
	Item		Criterion for defects		Defect type
(4)	Display pattern	Note: 1) Up to 3 dam 2) NG if there	C $D + E$ $C$ anages acceptable are two or more pin $D$		Minor
(5)	Spot-like contrast irregularity	ŕ	0.5	50mm each other	Minor
			·	eptable number	
		Size		opiacio iiuilioci	
		Size < <	`	*	
(6)	Bubble in		0.20	Ignore 3	Minor
(6)	Bubble in polarizer	< ≤	0.20	Ignore	Minor



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1			
		Note: 1) Conformed to limit samples.	
		2) Defects separate at intervals of 50mm each other	
(7)	Scratches and dent on the polarizer	Scratches and dent on the polarizer shall be in the accordance with "2) Black/White spot, 3) Black/White line".	Minor
(8)	Stains on LCD panel surface	Stains which cannot be removed even when wiped lightly with a soft cloth or similar cleaning too.	Minor
(9)	Rainbow color	No rainbow color is allowed in the optimum contrast on state within the active area.	Minor
(10)	Viewing area encroachm ent	Polarizer edge or line is visible in the opening viewing area due to polarizer shortness or sealing line.	Minor
(11)	Bezel appearance	Rust and deep damage which are visible in the bezel is rejectable.	Minor
(12)	Defect of land surface contact (Poor soldering)	Evident crevices which is visible are rejectable	Minor
(13)	Part mounting	<ol> <li>Failure to mount parts</li> <li>Parts not in the specifications are mounted</li> <li>Polarity, for example, is reversed</li> </ol>	Major
(14)	Parts alignment	<ol> <li>LSI, IC lead width is more then 50% beyond pad outline.</li> <li>Chip component is off center and more then 50% of the leads is off the pad outline.</li> </ol>	Minor
(15)	Conductive foreign matter (Solder ball, Solder chips)	<ol> <li>0.45 &lt; , N≥1</li> <li>0.30 &lt; ≤ 0.45, N≥1, : Average diameter of solder ball (Unit: mm)</li> <li>0.50 &lt; L, N≥1, L: Average length of solder chip (Unit: mm)</li> </ol>	Major Minor Minor
(16)	PWB pattern correction	<ol> <li>Deep damage is found on copper foil and the pattern is nearly broken.</li> <li>Damage on copper foil other than 1. above</li> </ol>	Major Minor
(17)	Faulty PWB correction	<ol> <li>Due to PWB copper foil pattern burnout, the pattern is connected, using a jumper wire for repair; 2 or more places are corrected per PWB.</li> <li>Short circuited part is cut, and no resist coating has been performed.</li> </ol>	Minor
(19)	Bezel Claw	Bezel claw missing or not bent	Minor



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(19) Indicat	on Rust and deep damage which are visible in the bezel is reflect	Minor
of nam	e able.	
plate		
(sampli	ng	
indicati	on	
label)		

# 6. Reliability

### 6.1 Life time

10,000 Hrs (The definition of half-brightness life is either average brightness reach to 50% of initial average brightness or lamp stopping light emission.)

# 6.2 Items of reliability

	Tterns of Terrac	, 1114 )	
	Item	Condition	Criterion
1.	High temperature operating	50°C, 96 hrs	No cosmetic failure are allowable.
_	1 0		Contrast ratio should be between initial value ± 10%
2.	Low temperature operating	0 °C, 96 hrs	Total current consumption should be below double of initial value.
3.	Humidity	40 °C, 90%RH, 96 hrs	
4.	High temperature	70°C, 96 hrs	No cosmetic failure are allowable.
5.	Low temperature storage	-20 °C, 96 hrs	Contrast ratio should be between initial value ± 20%  Total current
6.	Thermal shock storage	25 °C → 20 °C → 25 °C → 70 °C 5(min) 30(min) 5(min) 30(min) 5 cycle, 55 ~ 60%RH	consumption should be below double of initial value.
7.	Vibration	$10 \sim 55 \sim 10 \text{hz}$ amplitude; 1.5mm 2hrs for each direction (X, Y, Z)	No cosmetic failure are allowable.  Total current consumption should be below double of initial value.



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### 7. Handling Precautions

### 7.1 Mounting Method

The LCD panel of APEX SCIENCE & ENGINEERING CORP. LCD module consists of two thin glass plates with polarizers which easily get damaged and the module is so constructed as to be fixed by utilizing fitting holes in the plastic cases. Extreme care should be used when handling the LCD modules.

### 7.2 Caution of LCD handling are cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- ♦ Isopropyl alcohol
- ♦ Ethyl alcohol
- ◆ Trichlorotriflorothane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- **♦** Ketone
- **♦** Aromatic

### 7.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you; Connect any unused input terminal to  $V_{DD}$  or  $V_{SS}$ , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

### 7.4 Packing

Module employ LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

To prevent modules from degradation, Do not operate or store them exposed direct to sunshine or high temperature/ humidity.

### 7.5 Caution for operation

It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.

However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.



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If the display area is pushed hard during operation, Some font will be abnormally displayed but it resumes normal condition after turning off once. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

### 7.6 Storage

In the case of storing for a long period of time (for instance, for years) for the purpose or replacement use, the following ways are recommended.

- ◆ Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- ◆ Placing in a dark place where neither exposure to direct sunlight nor light is, keeping the storage temperature.
- ◆ Storing with no touch on polarizer surface by the anything else. (It's recommended to store them as they have been contained in the inner container at the time of delivery from use.)

### 7.7 Safety

- ◆ It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- ♦ When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.



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### 8. Precautions for use

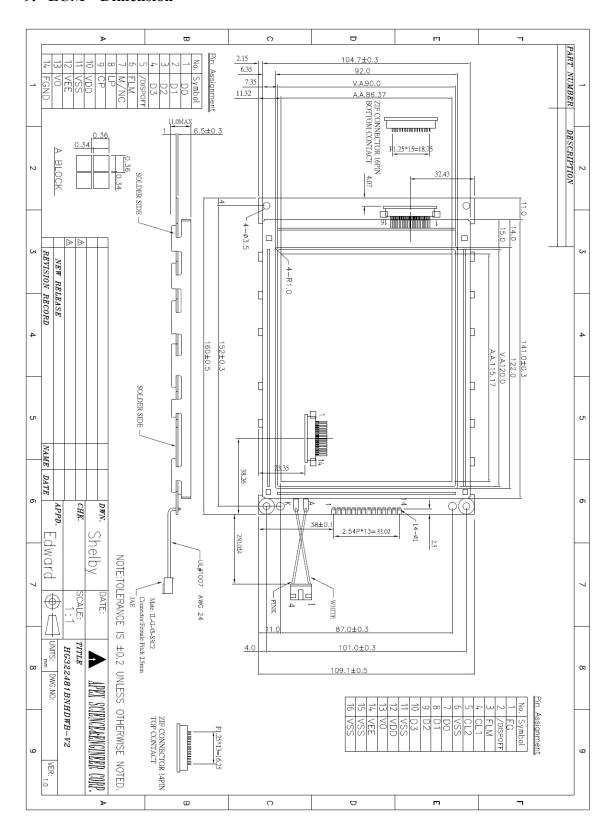
- 8.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity.Judgement by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- 8.2 On the following occasions, the handling of problem should be decided through discussion and agreement between responsible of the both parties.
  - ◆ When a question is arisen in this specifications.
  - ◆ When a new problem is arisen which is not specified in this specifications.
  - ◆ When an inspection specification change or operating condition change in customer is reported to APEX, and some problem is arisen in this specification due to the change.
  - ◆ When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.



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			A	Feb.25,13

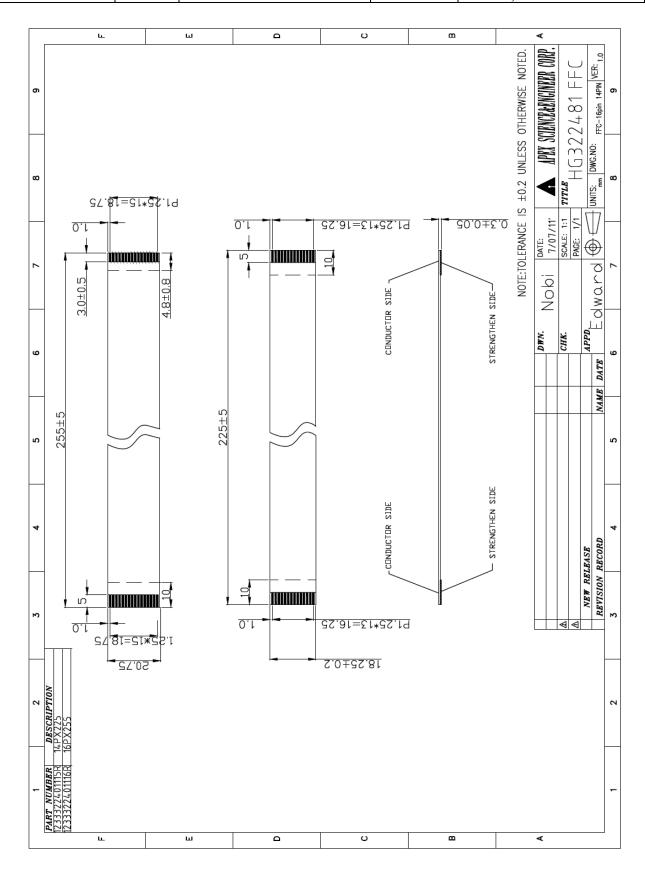
### 9. LCM Dimension





1 3/0 2 / 2 5 2 3 / 24

Messrs.				
<b>Product Specification</b>	Model	HG322481BNHDWB-V2	Rev. NO.	Issued Date.
	Miduci.	110322401BN11D W B- V 2	A	Feb.25,13





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